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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/611,518	07/07/2000	Jae-Yoel Kim	678-509 (P9463)	6612
28249	7590	12/28/2005	EXAMINER	
DILWORTH & BARRESE, LLP			COLIN, CARL G	
333 EARLE OVINGTON BLVD.			ART UNIT	
UNIONDALE, NY 11553			PAPER NUMBER	

2136

DATE MAILED: 12/28/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

## Office Action Summary

Application No.

09/611,518

Applicant(s)

KIM ET AL.

Examiner

Carl Colin

Art Unit

2136

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

### Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).
- Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

### Status

- 1) ☒ Responsive to communication(s) filed on 03 October 2005.
- 2a) ☒ This action is **FINAL**. 2b) ☐ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

### Disposition of Claims

- 4) ☒ Claim(s) 1,21 and 31-53 is/are pending in the application.
- 4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.
- 5) ☐ Claim(s) \_\_\_\_\_ is/are allowed.
- 6) ☒ Claim(s) 1,21 and 31-53 is/are rejected.
- 7) ☐ Claim(s) \_\_\_\_\_ is/are objected to.
- 8) ☐ Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

### Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 07 July 2000 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.  
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
- 11) ☐ The proposed drawing correction filed on \_\_\_\_\_ is: a) ☐ approved b) ☐ disapproved by the Examiner.  
If approved, corrected drawings are required in reply to this Office action.
- 12) ☐ The oath or declaration is objected to by the Examiner.

### Priority under 35 U.S.C. §§ 119 and 120

- 13) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).  
a) ☒ All b) ☐ Some \* c) ☐ None of:  
1. ☒ Certified copies of the priority documents have been received.  
2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.  
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).  
\* See the attached detailed Office action for a list of the certified copies not received.
- 14) ☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. § 119(e) (to a provisional application).  
a) ☐ The translation of the foreign language provisional application has been received.
- 15) ☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. §§ 120 and/or 121.

### Attachment(s)

- 1) ☒ Notice of References Cited (PTO-892) 4) ☐ Interview Summary (PTO-413) Paper No(s). \_\_\_\_\_
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948) 5) ☐ Notice of Informal Patent Application (PTO-152)
- 3) ☐ Information Disclosure Statement(s) (PTO-1449) Paper No(s) \_\_\_\_\_ 6) ☐ Other: \_\_\_\_\_

## DETAILED ACTION

### *Response to Arguments*

1. In response to communications filed on 10/3/2005, claims 1, 21, 40-46, 48, and 51, have been amended. The following claims 1, 21, and 31-53 are presented for examination.

1.1 In response to communications filed on 10/3/2005, Applicant has amended the specification and claims 1, 21, 40-46 to overcome the objection from the last Office Action. The objection to the specification has been withdrawn with respect to the amendment. The 112<sup>th</sup> rejection to claims 31, 32, 38, 39 has been withdrawn.

1.2 Applicant's arguments, pages 11-15, filed on 10/3/2005, with respect to the rejection of claims 1, 21, and 31-53 have been fully considered, but they are not persuasive. Regarding claims 1 and 21, Applicant states, "the description of Dahlman fails to disclose the subject matter of the present invention of managing a scrambling code, i.e., the relationship between scrambling codes, and a method for assigning a scrambling code." In response to applicant's argument that the references fail to show certain features of applicant's invention, it is noted that the features upon which applicant relies (managing a scrambling code, i.e the relationship between scrambling codes and a method for assigning a scrambling code) are not recited in the rejected claims 1 and 21. Although the claims are interpreted in light of the specification, limitations from the specification are not read into the claims. See *In re Van Geuns*, 988 F.2d 1181, 26 USPQ2d 1057 (Fed. Cir. 1993). Applicant argues that col. 3 of Burns

Art Unit: 2136

generates a local PN code identically used by all base stations, and then performs masking for applying a unique offset to the local PN code for each base station. However, in the present invention, each base station uses a specific PN code (or a scrambling code), therefore the masking is applied to an m-sequence, not to the generated PN code (scrambling code), which is distinguishable from Burns. Further, it is respectfully submitted that Burns fails to disclose a method for managing a scrambling code and the relationship between scrambling codes, to which the present invention is directed. Examiner respectfully disagrees. Burns cites (column 6, lines 1-14) that the invention is not limited to a specific embodiment as mentioned above by Applicant, the PN code sequence may be the same or different code phase offset with respect to a reference, the I-channel and the Q-channel may be different PN code sequences... Applicant states "the masking step shifts the first m-sequence cyclically by L chips to generate an Lth secondary scrambling code associated with the primary scrambling code" is patentably distinct from Burns. Examiner respectfully disagrees. Burns does disclose cyclic shifting by L chips to generate scrambling codes and the combined references as explained in the office action render the claims obvious. In response to applicant's argument that there is no motivation nor would it have been obvious to add the matched filter vectors of Burns, i.e., the alleged secondary sequence, with a second m-sequence to produce a secondary scrambling code, the test for obviousness is not whether the features of a secondary reference may be bodily incorporated into the structure of the primary reference; nor is it that the claimed invention must be expressly suggested in any one or all of the references. Rather, the test is what the combined teachings of the references would have suggested to those of ordinary skill in the art. See *In re Keller*, 642 F.2d 413, 208 USPQ 871 (CCPA 1981). In addition the Office action is not combining the

Art Unit: 2136

references in order to add matching filters (see rejection below). Regarding claims 48 and 51, in response to applicant's arguments against the references individually, one cannot show nonobviousness by attacking references individually where the rejections are based on combinations of references. See *In re Keller*, 642 F.2d 413, 208 USPQ 871 (CCPA 1981); *In re Merck & Co.*, 800 F.2d 1091, 231 USPQ 375 (Fed. Cir. 1986). Regarding claims 48 and 51, Applicant argues that the 112 rejection is improper and the object of the invention is assigning scrambling codes to base stations. Applicant adds that the specification on page 18, line 25 to page 19, line 18 fully describes how the scrambling codes are used for data transmission, characteristics of the scrambling codes, and relationships between the scrambling codes. More specifically, page 19, lines 5 to 10 of the specification fully describes the utilization of the scrambling codes as recited in Claim 48. Examiner respectfully disagrees. There is no mention of first and second base stations nor there is description of assigning codes to first and second base stations. The description of figure 9 that Applicant is referring to is: " Fig. 9 is a diagram showing the structure of primary and secondary scrambling codes in accordance with a second embodiment of the present invention. At the time the invention was made, Applicant's invention was focused on "generating scrambling code in UMTS mobile communication system" as specified on the title and as summarized in the abstract. In fact, Applicant's abstract has mentioned nothing about assigning codes and no disclosure about base stations. The step of managing and the step of assigning scrambling codes as claimed in claims 48 and 51 are not disclosed in the specification and they are still considered as new matter. Therefore, applicant has not overcome the rejection by replacing the word managing for assigning and by

amending the preamble of claim 48. Examiner maintains the rejection of claims 1, 21, and 31-53 in view of the same references.

*Claim Rejections - 35 USC § 112*

2. The following is a quotation of the first paragraph of 35 U.S.C. 112:

The specification shall contain a written description of the invention, and of the manner and process of making and using it, in such full, clear, concise, and exact terms as to enable any person skilled in the art to which it pertains, or with which it is most nearly connected, to make and use the same and shall set forth the best mode contemplated by the inventor of carrying out his invention.

2.1 Claims 48, 51, and the intervening claims are rejected under 35 U.S.C. 112, first paragraph, as failing to comply with the written description requirement. The claim contains subject matter, which was not described in the specification in such a way as to reasonably convey to one skilled in the relevant art that the inventor(s), at the time the application was filed, had possession of the claimed invention.

Claims 48 and 51 as amended recite “generating a primary scrambling code for a first base station by adding ....” and “generating a secondary scrambling code for a first base station by adding ....” “Scrambling codes assigned to the first base station ... and Scrambling codes assigned to the second base station ...” “scrambling code group having  $((K-1)*M+K)$ th through  $(K*M+K)$ th scrambling codes assigned to the first base station and having  $((K-1)*M+K)$ th through  $((K+1)*M+K+1)$ th scrambling codes assigned to a second base station”... The disclosure on page 18, line 19 through page 19 does not describe claims 48 and 51 as claimed

Art Unit: 2136

and constitute new matter. The method for managing and steps of assigning to first and second base stations in these claims are not described in the specification as claimed. Applicant is required to cancel the new matter in the reply to this Office Action. Claims 50 and 53 are not disclosed either as claimed. Claims 49-50 and 52-53 depend directly on claims 48 and 51 and are rejected on the same rationale.

***Claim Rejections - 35 USC § 103***

3. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

3.1 **Claims 1, 21, 31-53** are rejected under 35 U.S.C. 103(a) as being unpatentable over US Patent 6,339,646 to **Dahlman et al.** in view of US Patent 6,141,374 to **Burns**.

3.2 **As per claims 1, 21, 34, and 41, Dahlman et al.** substantially teaches an apparatus for generating a primary scrambling code (see figure 4) comprising: a first m-sequence generator to generate a first a first m-sequence, by using a plurality of registers with values  $a_i$  wherein ( $i = 0$

Art Unit: 2136

to  $c-1$  and where  $c$  is the total number of the first registers) (see column 4, line 59 through column 5, line 16); a second m-sequence generator for generating a second m-sequence, by using a plurality of second registers with second-shift register values  $j = 0$  to  $c-1$  where  $c$  = the total number of second registers) (see figure 4); a first adder to add the first m-sequence and the second m-sequence to generate a primary scrambling code (see figure 4); **Dahlman et al** also suggests using plurality of adders for combining channelization codes and scrambling codes to produce other secondary scrambling codes or use a modified value or different code phase and not limited to any variations and rearrangements (see column 5, lines 18-35 column 4, lines 40-57). **Dahlman et al** discloses masking step adapted to shift m-sequence cyclically by  $L$  chips to generate a number of secondary scrambling associated with a primary scrambling code (column 3, line 60 through column 4, line 6). **Dahlman et al** does not explicitly disclose masking shifting the sequences cyclically by multiplying with a mask value. **Burns** in an analogous art teaches a masking section to mask the first shift register values  $a_i$  with a first set of mask values  $K_i$  to generate a third m-sequence and further discloses a masking section shifts m-sequence cyclically by  $L$  chips to generate a number of secondary scrambling associated with a primary scrambling code and further discloses adding the masking sequence with another sequence (see column 3, line 40 through column 4, line 5) and also discloses the masking is expressed by multiplying mask codes by register values (see **Burns**, column 8, lines 29-67 and figures 3 and 4).

Therefore, it would have been obvious to one of ordinary skill in the art at the time the invention was made to implement the masking concept of multiplying as disclosed in **Burns** before adding the sequence in **Dahlman** to generate secondary scrambling codes associated with the primary scrambling codes. The motivation to do so is given by **Dahlman** who suggests that additional



Art Unit: 2136

scrambling codes can be used to increase the number of available codes to avoid a shortage of codes (column 1, lines 36-48) and **Burns** suggests shifts are multiplied to produce new values then the combined masks are then added to produce new state values (column 8, lines 45-61).

As per claims 31-32 and 38-39, the combination of **Dahlman et al** and **Burns** discloses N number of primary scrambling codes and a total number of secondary scrambling codes for each primary code and further discloses a plurality of secondary scrambling codes associated with each primary scrambling code that meets the recitation of wherein the primary scrambling code is one of a plurality of primary scrambling codes and a Kth primary scrambling code is a  $((K-1)*M + K)$ th gold code where M is a total of secondary scrambling codes per primary scrambling code and K is between 1 and 512 and wherein the secondary scrambling codes associated with a Kth primary scrambling code are from  $((K-1)*M + K + 1)$ th to  $((K-1)*M + K + 1)$ th gold codes where M is a total of secondary scrambling codes per primary scrambling code and K is between 1 and 512 (see **Dahlman et al**, column 3, line 60 through column 4, line 6 and lines 40-44).

As per claims 33 and 36, the combination of **Dahlman et al** and **Burns** discloses generating a plurality of secondary codes for one set of primary scrambling code (see **Dahlman et al**, column 5, lines 3-18 and lines 43-47).

Claims 35 and 40 recite similar limitations to claims 1 and 21 performing the same method to generate additional code by using an additional masking value for generating an

Art Unit: 2136

additional sequence. Both references disclose generating Nth secondary codes. Therefore, claims 35 and 40 are still obvious since this modification requires routine skill in the art, these claims are rejected on the same rationale as the rejection of claims 1 and 21.

As per claims 37 and 47, the combination of **Dahlman et al** and **Burns** discloses the limitation of further comprising the step of delaying at least one of the primary scrambling code and secondary scrambling code to produce a Q-channel component wherein the primary scrambling code and secondary scrambling code are I-channel components (see **Burns**, column 6, lines 1-14 and column 3, lines 53-57). Therefore, these claims are rejected on the same rationale as the rejection of claims 1 and 21.

As per claim 42, the combination of **Dahlman et al** and **Burns** discloses the limitation of wherein the first m-sequence generator is adapted to cyclically shifting the first shift register values and the second m-sequence generator is cyclically shifts the second shift register values (see **Dahlman et al**, figure 4 and **Burns** column 8, lines 45-61 and see figure 4).

As per claims 43 and 45, the combination of **Dahlman et al** and **Burns** discloses the limitation of wherein the first m-sequence generator is adds predetermined bits of the first shift register values of the first shift registers based on the first generator polynomial of the first m-sequence, right shifting the first shift register values  $a_i$  of the first shift registers and replacing the first register value  $a_{i-1}$  with the result of the addition of the predetermined register values (see **Dahlman et al**, figure 4 and **Burns** column 8, lines 45-61 and see figure 4).

As per claims 44 and 46 Dahlman et al. discloses the limitation of wherein the first m-sequence generator is adds the first shift register value  $a_0$  with  $a_7$  to form a next  $a_{c-1}$  and  $b_0$  is added with  $b_5$ ,  $b_7$ , and  $b_{10}$  to form a next  $b_{c-1}$  (see figure 4).

4. Claims 48-53 are rejected under 35 U.S.C. 103(a) as being unpatentable over US Patent 6,339,646 to Dahlman et al in view of Foreign Patent Publication WO 99/12284 to Dahlman et al.

4.1 As per claim 48, Dahlman et al substantially teaches a method, comprising: generating a primary scrambling code for a first base station (column 3, line 60 through column 4, line 6) by adding a first m-sequence and a second m-sequence (figure 4); discloses N number of primary scrambling codes and a total number of secondary scrambling codes for each primary code and further discloses a plurality of secondary scrambling codes associated with each primary scrambling code the primary codes generated by shifting the first m-sequence by a number of chips is also disclosed in figure 4, that meets the recitation of generating a secondary scrambling code for the first base station, the generated secondary scrambling code belonging to a scrambling code group having  $((K-1)*M+K)$ th through  $(K*M+K)$ th scrambling codes assigned to the first base station and having  $((K-1)*M+K)$ th through  $((K+1)*M+K+1)$ th scrambling codes assigned to a second base station and having wherein M is a total number of secondary scrambling codes per primary scrambling code and K is a natural number, the  $((K-1)*M+K)$ th and  $(K*M+K+1)$ th scrambling codes being the primary scrambling code of the first base station

Art Unit: 2136

and a primary scrambling code of the second base station, respectively, and the  $((K-1) * M + K + 1)$ th through  $((K+1) * M + K + 1)$ th scrambling codes are generated by shifting the first m-sequence by  $((K-1) * M + K)$  through  $((K+1) * M + K)$  chips, respectively; (column 3, line 60 through column 4, line 6 and figure 4)

adding the shifted first m-sequence to the second m-sequence, wherein the  $((K-1) * M + K + 1)$ th through  $((K+1) * M + K + 1)$ th scrambling codes that are generated by shifting the first m-sequence by  $((K-1) * M + K)$  through  $((K+1) * M + K)$  chips, respectively, are used to separate the  $((K-1) * M + K)$ th through  $(K * M + K)$ th scrambling codes assigned to the first base station from the  $(K * M + K + 1)$ th through  $((K+1) * M + K + 1)$ th scrambling codes assigned to the second base station (see figure 4).

**Dahlman et al** does not explicitly disclose the assigning to a second base station but refers to a prior art where the managing and assigning of multiple codes are disclosed (column 1, lines 35-48).

**Dahlman et al** in Foreign Patent Publication WO 99/12284 discloses assigning spreading codes to base stations (see WO 99/12284 figure 5, 7, and 8 with description). Therefore, it would have been obvious to one of ordinary skill in the art at the time the invention was made to use the method described in US Patent 6,339,646 to **Dahlman et al** to generate the scrambling codes and combining it with the teaching in the Foreign Publication WO 99/12284 to assign the codes to the base stations. The motivation or suggestion to do so is given by **Dahlman** who suggests that additional scrambling codes can be used to increase the number of available codes to avoid a shortage of codes and refers to the foreign publication for generating and assigning multiple code sets (column 1, lines 36-48).

**Claim 51** recites similar but broader limitations to claim 48. The method of assigning is disclosed by **Dahlman** in the Foreign Patent Publication. Therefore claim 51 is rejected on the same rationale as the rejection of claim 48.

**As per claims 49-50 and 52-53, Dahlman et al** discloses shift register memory for storing scrambling codes and bits of data (see column 4, line 59 through column 5, line 16 and figure 4); the scrambling codes of these claims are similar to the scrambling codes in claims 48 and 51 and are rejected on the same rationale.

#### *Conclusion*

5. **THIS ACTION IS MADE FINAL.** Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire **THREE MONTHS** from the mailing date of this action. In the event a first reply is filed within **TWO MONTHS** of the mailing date of this final action and the advisory action is not mailed until after the end of the **THREE-MONTH** shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than **SIX MONTHS** from the mailing date of this final action.

Art Unit: 2136


5.1 The prior art made of record and not relied upon is considered pertinent to applicant's disclosure as the art discloses multiple access coding for radio communications.


US Patents: 5,771,288 Dent et al ; 3,818,442 Solomon ; 6,728,305 Ogawa et al ;  
6,728,411 Ogawa et al ; 6,108,369 Ovesjo et al ; 6,459,722 Sriram et al ; 6,574,205 Sato.

5.2 Any inquiry concerning this communication or earlier communications from the examiner should be directed to Carl Colin whose telephone number is 571-272-3862. The examiner can normally be reached on Monday through Thursday, 8:00-6:30 PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Ayaz Sheikh can be reached on 571-272-3795. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

  
Carl Colin  
Patent Examiner  
December 21, 2005

  
Primary Examiner  
AU 2136  
12/21/05